

NAK1-BG86



PATENT APPLICATION

#1
CBrus
2-128

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Shuichi Takayama et al.

Serial No.: 09/301,284

Filed: April 28, 1999

For: PROCESSOR FOR EXECUTING
INSTRUCTIONS IN UNITS THAT ARE
UNRELATED TO THE UNITS IN WHICH
INSTRUCTIONS ARE READ AND A
COMPILER, AN OPTIMIZATION
APPARATUS, AN ASSEMBLER, A LINKER,
A DEBUGGER, AND A DISASSEMBLER
FOR SUCH A DEBUGGER

Examiner: T. Vo

RECEIVED

Group Art Unit: 2122 FEB 06 2002

Technology Center 2100

January 7, 2002

Irvine, California 92614

REQUEST FOR DRAWING CHANGE APPROVAL

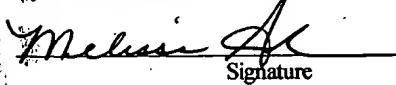
Honorable Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

The attached proposed corrections to FIGS. 35A and 35B are submitted for review and approval in response to the Official Action dated August 7, 2001. The corrections merely space apart FIG. 35A and FIG. 35B to distinctly separate the figures as requested in the Official Action. These corrections do not add new matter and approval is requested.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231, on January 7, 2002.

By: Melissa Sanchez


Signature

Date: January 7, 2002

accepted by Examiner
4/12/02
T.V.

Respectfully submitted,

PRICE AND GESS



Bradley D. Blanche
Registration No. 38,387
2100 S.E. Main St., Suite 250
Irvine, California 92614
Telephone: 949/261-8433